Reading list for Hardware Design for Machine Learning Acceleration, TSEA85, 2026

Additional literature

Books

Crockett H Louise (Author), Northcote David (Author), Ramsay Craig (Author), (2019) *Exploring Zynq MPSoC: With PYNQ and Machine Learning Applications*
1st Strathclyde Academic Media
ISBN: 978-0992978761
This book introduces the Zynq MPSoC (Multi-Processor System-on-Chip), an embedded device from Xilinx and the PYNQ platform to design machine learning applications.
https://www.zynq-mpsoc-book.com/

Websites

Intro to FPGA design with HLS https://docs.amd.com/v/u/en-US/ug998-vivado-intro-fpga-design-hls

PYNQ (Python on Zynq) http://www.pynq.io/

VITIS HLS synthesis guide https://docs.xilinx.com/r/en-US/ug1399-vitis-hls

Zynq SoC https://www.xilinx.com/products/silicon-devices/soc/zynq-7000.html#documentation