

Litteraturlista för Systemkonstruktion, TSIU03, 2018

Böcker

Andrew Rushton, (2011) *VHDL for Logic Synthesis* 3rd edition John Wiley & Sons
ISBN: ISBN-13: 978-0470688472

Peter J. Ashenden, (2007) *Digital Design: An Embedded Systems Approach
Using VHDL* Morgan Kaufmann
ISBN: ISBN-13: 978-0123695284